

Notice of References Cited	Application/Control No. 10/530,425		Applicant(s)/Patent Under Reexamination GOOSSENS, KEES GERARD WI	
	Examiner SARAH E. DRABIK		Art Unit 2155	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,381,638	04-2002	Mahler et al.	709/220
*	B	US-2002/0144078	10-2002	Topham et al.	711/203
*	C	US-6,567,969	05-2003	Agrawal et al.	716/17
*	D	US-2005/0050297	03-2005	Essick et al.	711/207
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Guerrier, P. et al., "A Generic Architecture for On-Chip Packet Packet-Switched Interconnections", IEEE Conf. Proceedings, 278 March 2000, pages 250-256
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.